

# 123\SmartBMS<sup>gen3</sup>

## UART data protocol

Firmware v3.3.11

### General information

	Value
<b>UART info</b>	
Bitrate	9600 bps
Start bit	1
Stop bit	1
Parity bit	None
<b>Data info</b>	
Endianness	Big-endian
Transmit interval	1 s
Data bytes	58

### Data bytes

Byte	Information	Step	Size	Example
<b>1, 2, 3</b>	Total voltage	0.005 volt/bit	24 bit	0x0105FF = 335.4 volt
<b>4</b>	Sign byte I1	Ascii: +, -, X	8 bit	0x2B = +, 0x2D = -, 0x58 = X
<b>5, 6</b>	Current sensor 1 (I1)	0.125 amp/bit	16 bit	0x0100 = 32 amp
<b>7</b>	Sign byte I2	Ascii: +, -, X	8 bit	0x2B = +, 0x2D = -, 0x58 = X
<b>8, 9</b>	Current sensor 2 (I2)	0.125 amp/bit	16 bit	0x0100 = 32 amp
<b>10</b>	Sign byte I-sum	Ascii: +, -, X	8 bit	0x2B = +, 0x2D = -, 0x58 = X
<b>11, 12</b>	Total current (I1 – I2)	0.125 amp/bit	16 bit	0x0100 = 32 amp
<b>13, 14</b>	V lowest	0.005 volt/bit	16 bit	0x0230 = 2.80 volt
<b>15</b>	Cell nr V lowest	cell nr/bit	8 bit	0x32 = Cell number 50
<b>16, 17</b>	V highest	0.005 volt/bit	16 bit	0x0230 = 2.80 volt
<b>18</b>	Cell nr V highest	cell nr/bit	8 bit	0x32 = Cell number 50
<b>19, 20</b>	T lowest	1 deg / bit + offset	16 bit	0x0114 = 0 deg Celcius, 0x0128 = 20 deg Celcius
<b>21</b>	Cell nr T lowest	cell nr/bit	8 bit	0x32 = Cell number 50
<b>22, 23</b>	T highest	1 deg / bit + offset	16 bit	0x0114 = 0 deg Celcius, 0x0128 = 20 deg Celcius
<b>24</b>	Cell nr T highest	cell nr/bit	8 bit	0x32 = Cell number 50
<b>25</b>	Information of specific cell nr	cell nr/bit	8 bit	0x32 = Cell number 50
<b>26</b>	Nr of cells	cell nr/bit	8 bit	0xFF = Cell number 255

<b>27, 28</b>	Cell voltage of specific cell	0.005 volt/bit	16 bit	0x0230 = 2.80 volt
<b>29, 30</b>	Cell Temp of specific cell	1 deg / bit + offset	16 bit	0x0114 = 0 deg Celcius, 0x0128 = 20 deg Celcius
<b>31</b>	Status byte 1		8 bit	See table "Status byte 1"
<b>32, 33, 34</b>	TodayEnergy collected	Wh/bit	24 bit	0x000064 = 100 Wh
<b>35, 36, 37</b>	Energy stored	Wh/bit	24 bit	0x00F221 = 61.985 kWh
<b>38, 39, 40</b>	Today Energy consumed	Wh/bit	24 bit	0x000064 = 100 Wh
<b>41</b>	SoC %	1%/bit	8 bit	0x32 = 50%
<b>42, 43, 44</b>	Total collected	kWh/bit	24 bit	0x640000 = 6.553.600 kWh
<b>45, 46, 47</b>	Total consumed	kWh/bit	24 bit	0x640000 = 6.553.600 kWh
<b>48</b>	Key		8 bit	Offset 25, 0x19 = key 0
<b>49</b>	Value		8 bit	See table below
<b>50, 51</b>	Battery capacity	0.1 kWh/bit	16 bit	0x00A0 = 16.0 kWh
<b>52, 53</b>	V-MIN Setting	0.005 volt/bit	16 bit	0x0244 = 2.90 V
<b>54, 55</b>	V-MAX Setting	0.005 volt/bit	16 bit	0x02E4 = 3.70 V
<b>56, 57</b>	V-Balance Setting	0.005 volt/bit	16 bit	0x02BC = 3.50 V
<b>58</b>	Checksum		8 bit	Lowest 8 bits of sum of all received data bytes (57 bytes, except checksum)

## Status byte 1

Bit	Info
<b>7 (MSB)</b>	SoC not calibrated
<b>6</b>	Exceed T max
<b>5</b>	Exceed T min
<b>4</b>	Exceed V max
<b>3</b>	Exceed V min
<b>2</b>	Comm error
<b>1</b>	Allow to discharge
<b>0 (LSB)</b>	Allow to charge

### Status byte 2

Bit	Info
<b>7 (MSB)</b>	Unused
<b>6</b>	Unused
<b>5</b>	Relay "Load" closed/active
<b>4</b>	Relay "Charge" closed/active
<b>3</b>	Exceed Tmin discharge
<b>2</b>	Exceed Tmin charge
<b>1</b>	Early warning
<b>0 (LSB)</b>	Unused

### Key/value pairs (byte 48 and 49)

Key	Value	Step	Remarks
<b>0</b>	State-of-health (SoH)	1%/bit	
<b>1</b>	Charge efficiency	1%/bit	
<b>2</b>	Vlow MSB	5mV/bit	
<b>3</b>	Vlow LSB		
<b>4</b>	Vnom MSB	5mV/bit	
<b>5</b>	Vnom LSB		
<b>6</b>	Fw version major   minor		High nibble for major, low nibble for minor
<b>7</b>	Fw version patch		
<b>8</b>	Battery charge cycles MSB	1C/bit	
<b>9</b>	Battery charge cycles LSB		
<b>10</b>	Battery charged energy MSB	1kWh/bit	
<b>11</b>	Battery charged energy		
<b>12</b>	Battery charged energy LSB		
<b>13</b>	Battery discharged energy MSB	1kWh/bit	
<b>14</b>	Battery discharged energy		
<b>15</b>	Battery discharged energy LSB		
<b>16</b>	Status byte 2		See table "Status byte 2"
<b>17</b>	Vfull MSB	5mV/bit	
<b>18</b>	Vfull LSB		

## Electrical circuit

The End Board has an 2 pin "EXT OUT" connector with UART data output. This UART signal is inverted and galvanically connected to the End Board power, which is the last cell. If you are building your own circuit, it is highly recommended to isolate the signal, for example with an optocoupler. You can use most general purpose optocouplers, for example the FOD814 or FOD817.

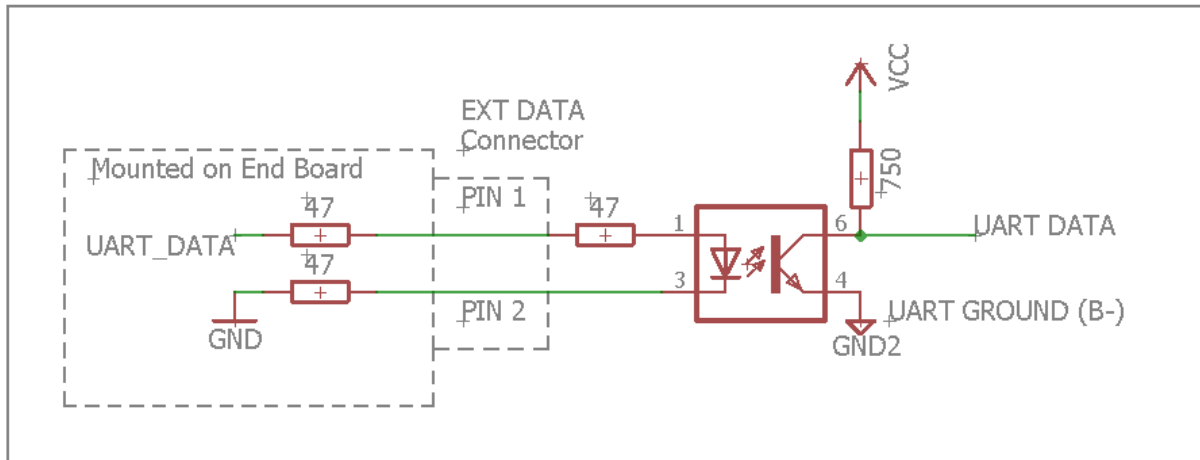


Figure 1 - Example optocoupler isolation circuit

Note: the 123\SmartBMS to USB cable does not need any additional isolation. The cable already contains an electrical isolation circuit.